PULNIX

TM-2016-8

General Description

The TM-2016-8 is a miniature, very high-resolution, 2 Megapixel, high-speed monochrome, progressive-scan CCD camera with a 16:9 aspect ratio (like HDTV). The imager resolution is 1920 x 1080 pixels and the frame rate is 8 frames per second. The interline transfer CCD permits full vertical and horizontal resolution of high speed shuttered images. The electronic shutter has speeds up to 1/3,500 sec. and can be reset asynchronously by external pulse control.

The TM-2016-8 has a patent-pending, PULNiX-exclusive, built-in look-up table (LUT). This full dynamic range-control function can be set at externally selectable knee slopes to optimize the CCD's full dynamic range in the normal output signal range. It also provides fast 10-bit to 8-bit conversion pre-processing for effective image feature enhancement. The camera has both analog and digital (RS-644) output for interfacing with frame grabbers.

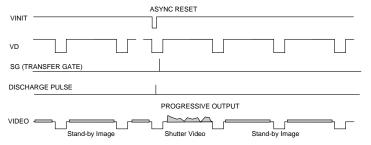
All camera control functions are externally controlled via a user-friendly RS-232C graphical interface provided by PULNiX. The TM-2016-8 has a pixel clock locking phase lock loop (PLL) for multi-camera operation which requires exact data and clock phases from each camera (optional).*

Applications for the TM-2016-8, which has a 16:9 aspect ratio and wide field of view, include machine vision, medical imaging, intelligent transportation systems, high-definition graphics, gauging, character recognition, documents archiving, surveillance, and aerial imaging.

Asynchronous Reset

The TM-2016's asynchronous reset is flexible and accepts external horizontal drive (HD) for phase locking. When the VINIT pulse is applied, the scanning is reset and a new frame will be generated. There are two asynchronously-reset shutter speed control modes:

- 1. External VINIT with pulse width. The duration between pulse edges controls the shutter speed externally.
- 2. Internal shutter speed control. The speed control varies from 1/35 to 1/3500 sec. The video signal and FDV starts with internal V reset timing related to shutter speed.





Product Summary

- High resolution, 1920(H) x 1080(V) at 8 frames per second
- I" progressive scan, interline transfer CCD imager for good dynamic range.
- Miniature 44x44x64 mm housing with high-rel connector
- Digital RS-644 (LVDS) output and analog output
- Maximum dynamic range control with PULNiXexclusive, patent-pending built-in look-up table (Gamma, knee, user parameters)
- Full frame integration, two-row binning (1920x540) pixels at 16 frames per second.
- Full-frame shutter to 1/3500 sec.
- Asynchronous reset with external shutter control
- RS-232 external control
- Pixel clock locking for precision multiplexing of cameras (optional)*

Electronic Shutter

The TM-2016-8 has a substrate drain-type shutter mechanism which provides a superb picture at various speeds without smearing. A built-in manual shutter speed control selects the electronic shutter rate of 1/35 (non-async mode only), 1/70, 1/140, 1/270, 1/510, 1/1050, 1/1600, 1/2500, or 1/3500 second.

The CCD discharges when discharge pulses are applied via internal shutter control. With a negative pulse to VINIT, the camera resets and purges the CCD charge momentarily. Then it starts integrating for the period of preset shutter control time by either an external pulse width or internal shutter control.

Progressive scanning permits a full 1080 lines of vertical resolution, as compared to a conventional CCD camera which captures only half the vertical lines per shutter.

Integration

The CCD imager of the TM-2016-8 can be exposed for longer than the normal scan timing of 1/8 sec. This integration feature provides extra sensitivity for dark-environment applications. The progressive-scan imager permits a full frame of resolution in non-interlace format. Integration is achieved either by: (1) direct control of INTEG signal pin 11 or (2) Pulse Width control, up to 1 sec, by VINIT on pin 9.

*option unavailable at present

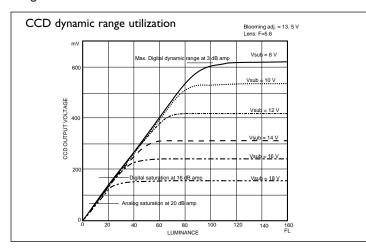
TM-2016-8 Progressive Scan 2K x 1K Shutter Camera

Dynamic Range Control

Typical interline transfer CCDs have fixed noise levels based on dark current (thermal or KT noise), pattern noise, and operating clock speed. Typically for a 2K x 1K CCD operating at 20MHz pixel clock, the noise level is around 30 electrons. The maximum capacity of the CCD charges is limited by the well capacity at saturation. The range is limited by the structure and the pixel size.

The TM-2016-8 uses a 1" CCD with 7.4 μ m x 7.4 μ m pixel and three-phase vertical shift register structure. The well capacity is 40,000 electrons. The theoretical dynamic range is 40,000:30 = 1333:1 (62.5 dB).

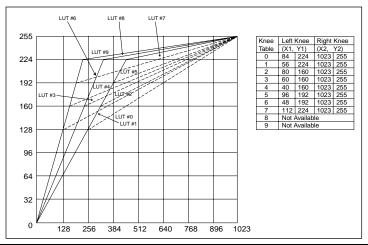
A typical CCD camera does not use the full dynamic range due to the nominal gain and the output specification such as RS-170. The typical CCD camera has its gain set at 16 to 22 dB and the RS-170 video level is 714 mV. Using 20 dB gain for the calculation, CCD output is limited to 714/10 = 71.4 mV. Since the CCD's saturation voltage is 400 mV to 500 mV, the system therefore uses less than 1/5 of the available dynamic range.



Machine vision and outdoor applications cannot afford to miss image information behind the saturation, which is why the dynamic range adaptation is critical.

Programmable LUT and Knee Control (patent pending)

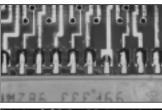
The TM-2016-8 has a built-in LUT (look-up table) for dynamic range control and preprocessing for feature

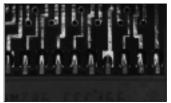


enhancements. At a specific gain setting, the offset (minimum level... dark point) and A/D reference top voltage (maximum level... saturation point) are set to 10-bit A/D input so that the full dynamic range of the CCD is utilized at 10-bit references as the input and the LUT output is converted into 8-bit to adjust the gamma correction.

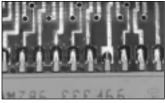
The 10-bit input is segmented into two or more regions by the knee point settings as variable gamma selection.

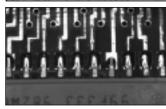
- LUT selections: (10 sets of standard knee values)
- 1 Variable knee curve
- 2 Direct input LUT





Linear image: When PCB surface is visible, metal trace is saturated. If the metal surface appears, then the plastic surface is too dark.





Knee-controlled image:The upper is LUT#6, the lower is #7. Both show the full dynamic range with different effects.

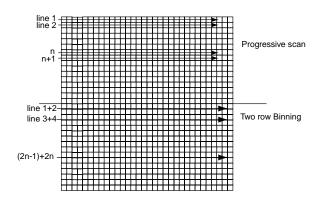
Scan Mode

Full Progressive Scan

The normal scanning mode for the TM-2016-8 is 1920 x 1080 pixels. The standard speed with single-channel output is 8 frame/sec at the pixel clock of 20.0 MHz. Unlike an interlace scan camera, the TM-2016-8 reads every line from top to bottom, resulting in all lines being obtained per captured image frame with electronic shutter.

Two-Row Binning

This is a standard feature in the TM-2016-8. The camera scans two rows together and outputs 1920 (H) x 540 (V) pixels at 16 frames per second. Two-row binning is typically implemented to increase the frame rate.



TM-2016-8 Progressive Scan 2K x 1K Shutter Camera

External Sync

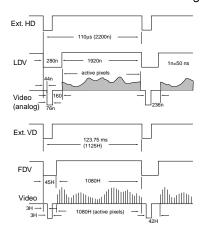
The TM-2016-8 accepts an external sync. of standard HD and VD at TTL level for general locking to a system sync. and clock. The external sync. is only available for 8-frame mode and the frequency requirement is as follows:

fHD = 9.09 KHz ±5% fVD = 8.0 Hz ± 5%

(Internal Master clock = 40.0 MHz,

Pixel clock = 20.0 MHz)

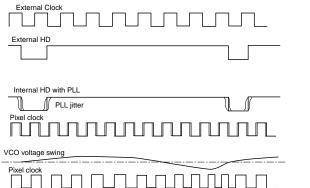
Please contact PULNiX for TM-2016-8 timing charts.



Pixel Clock Locking*

For multiple-camera applications such as 2D or 3D measurement, a parallel digital data input capability is important. The high resolution and precision measurement requires that camera data be consistent and free from phase lock loop (PLL) jitter. This is especially important for digital output multiplexing.

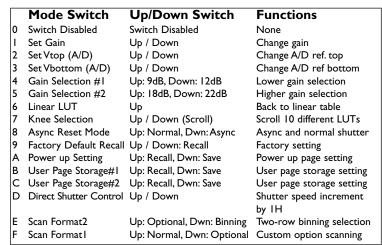
The TM-2016-8 has an optional capability for pixel clock locking. It takes RS-644 (differential) external clock and H and V reset sync (TTL or RS-644). By doing this, each camera's digital data is in-phase with the pixel clock (data clock) for multiplexing or parallel input to a frame grabber.



Mode Switches

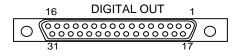
Various modes can be implemented with the rear panel-mode selection switch and Up/Down switch as well as RS-232 external control. When RS-232 is connected, the command over-writes the rear panel switch settings.





Connector and Pin Configurations

Digital Output Connector



3	I-Pin con	nector	(MP211-031-	-113-4300)	
Pin:	# Descripti	on I/O	`Pin#	Description	I/O
- 1	CLK+	Out	17	CLK-	Out
2	LDV+	Out	18	LDV-	Out
3	FDV+	Out	19	FDV-	Out
4	GND		20	VINIT	In
				[INTEG (TTL)	ln]
5	EXT HD (TT	TL) in	21	EXT VD (TTL)	
	[EXT CLK+	ln]		[EXT CLK-	ln]
6	INTEG	In	22	N/C	
	[HD+	ln]		[HD-	ln]
7	N/C		23	GND	
	[Vinit+/(VD+	·) In]		[Vinit-/(VD-)]	ln]
8	D0+	Out	24	D0-	Out
9	DI+	Out	25	DI-	Out
10	D2+	Out	26	D2-	Out
П	D3+	Out	27	D3-	Out
12	D4+	Out	28	D4-	Out
13	D5+	Out	29	D5-	Out
14	D6+	Out	30	D6-	Out
15	D7+	Out	31	D7-	Out
16	GND				

Note: CLK: data clock, LDV: Line data valid, FDV: Frame data valid, INTEG: Integration control, EXT CLK: external pixel clock, []: Differential input option

12-Pin Connector						
I GND (power)	7	VD in				
2 +12V "	8	Strobe+ out				
3 GND (analog)	9	HD in				
4 Video out	10	RXD(RS232)				
5 GND (digital)	П	Int. cont				
6 VINIT in	12	TXD(RS232)				
		, ,				





	Shutter (Manual	Control Switch Async
0	no shutter	no shutter
- 1	1/35	1/3,500
2	1/70	1/2,500
3	1/140	1/1,600
4	1/270	1/1,050
5	1/510	1/510
6	1/1,050	1/270
7	1/1,600	1/140
8	1/2,500	1/70
9	1/3,500	Ext. pulse width
		control

*option unavailable at present

SPECIFICATIONS

Imager	I" progressive scan interline transfer CCD	AGC	OFF	
Active Area	14.2mm x 8.0mm	Gamma	Programmable LUT(1.0 std.)	
Active Pixels	1920 (H) x 1080 (V), 16:9 aspect ratio	Lens mount	C-mount (use I" format lenses)	
Cell size	7.4 μm x 7.4 μm	Power req.	12V DC, 440 mA (current measured at 25°)	
Display Modes	1920 (H) x 1080 (V) @ 8Hz (normal scan)	Operating temp.	-10°C to 45°C*	
(Active Pixels)	1920 (H) x 540 (V) @ 16Hz (two-row binning)	Vibration	7Grms (10 Hz to 2000 Hz) Random	
Sync	Internal/external auto switching	Shock	70G	
	HD/VD, 4.0 Vp - p impedance 4.7 $KΩ$	Size (W x H x L)	44mm x 44mm x 64mm (1.73" x 1.73" x 2.51")	
	VD=8 Hz ±5%, non-interlace, HD=9.09 kHz±5%	Weight	136 grams, 4.8 oz	
Data clock output		Functional options	adjustable back-focus front end	
Resolution	Digital: 1920 (H) x 1080 (V), Analog: over 1200 TV lines (H) x 800 TV lines (V)	* Image quality will degrade with increasing temperature.		
S/N ratio	50dB min.	Optional accessories	(must be ordered separately)	
Min. illumination	2.0 lux, f=1.4 without IR cut filter (no shutter) Sensitivity: 13µV/e-	I/O Power cable	30DG-02 digital output cable CS-232E serial communication kit	
Video output	Analog: I.0 Vp-p composite video, 75Ω Digital output: 8-bit RS-644	Power supply	12P-02S (Optional) PD-12UUP series (includes power connector)	

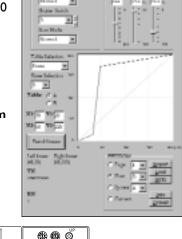
Graphical User Interface

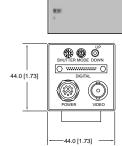
A user-friendly graphical user interface (GUI) of TM-1020 camera control is provided in the CS-232E kit. This interface allows users to control the following functions of the TM-2016-8 camera:

- Shutter control for manual, async. and pulse width control
- Gain control
- A/D reference voltages control for Vtop and Vbottom

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- Save settings
- Load settings
- Report settings
- LUT setting and graphic display
- Scanning mode selection and Option selections

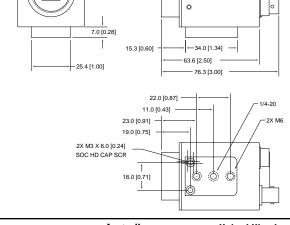




Camera parameters can be uploaded from the PC to the camera. Once these parameters are stored in EEPROMs, an instantaneous change from one setting to another can be done with a delay of a few frames in between.

Serial Communication

The serial communication kit CS-232E consists of serial cable RS-232B-12, a software disk, and a quick-start guide. The RS-232B-12 cable has a 12-pin connector on the camera end, and a 9-pin d-sub connector (RS-232) and a 12-pin connector (power and sync signals) on the other end.









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Innovation

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